

Can low-voltage CMOS technology be used for a switch-capacitor converter?

Low-voltage design techniques for the switched-capacitor building blocks have been demonstrated enabling the implementation of larger applications such as sample-and-holds, filters, and data converters. In particular, a 1.5 V, 10-bit, 14.3 MS/s, 36 mW pipeline analog-to-digital converter was implemented in a 0.6 μ m CMOS technology.

What is the capacitance of linear capacitors?

Linear capacitors were implemented using poly over n⁺-diffusion in a n-well. The capacitance of these capacitors was approximately 2.3 fF/ μ m. The substrate consisted of low-resistance p⁺ with an epitaxial layer of p⁻ on top.

Are MOS device reliability issues relevant to switched-capacitor circuits?

identifying the MOS device reliability issues that are relevant to switched-capacitor circuits, (2) introduction of a new bootstrap technique for operating MOS transmission gates on a low voltage supply without significantly degrading device lifetime, (3) development of low-voltage opamp design techniques.

What CMOS technology is used for low-voltage circuits?

AS A DEMONSTRATION of the low-voltage circuit techniques described in the previous chapters, a 1.5V, 10-bit, 14.3MS/s analog-to-digital converter prototype was implemented in a 0.5 μ m CMOS technology. This chapter discusses specific implementation details including schematics and layout issues.

What is a switched capacitor DAC?

Unlike resistor-string DACs, a switched capacitor array does not consume any static power. Furthermore, the charge domain nature of the capacitor DAC complements switched-capacitor integrators in many applications such as sigma-delta modulators and pipeline ADCs. Figure 3.11 shows one implementation of a capacitor DAC.

What is a switched capacitor array?

Using the principle of charge division, an array of capacitors can be used to perform digital-to-analog conversion. Unlike resistor-string DACs, a switched capacitor array does not consume any static power.

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